Docket No. AUS920030707US1 Aguilar, et al.

System and Meth d for Managing a Plurality of Pr c ssors as Devices

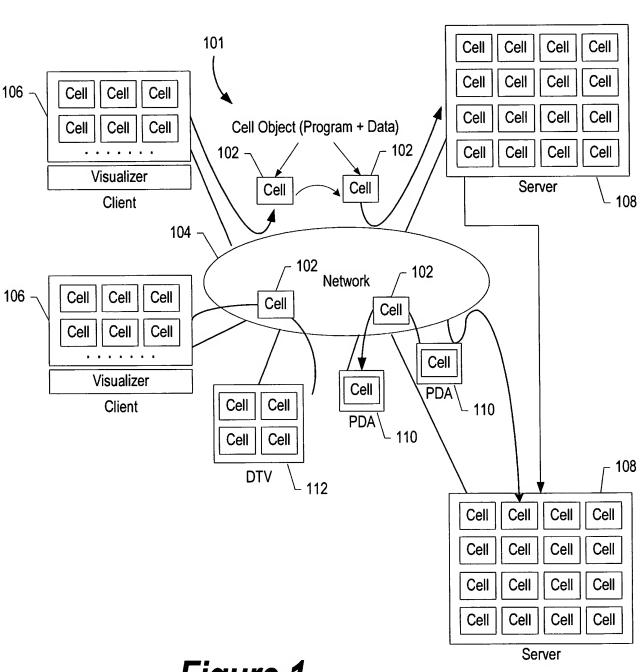


Figure 1

D cket N . AUS920030707US1

Aguilar, et al.

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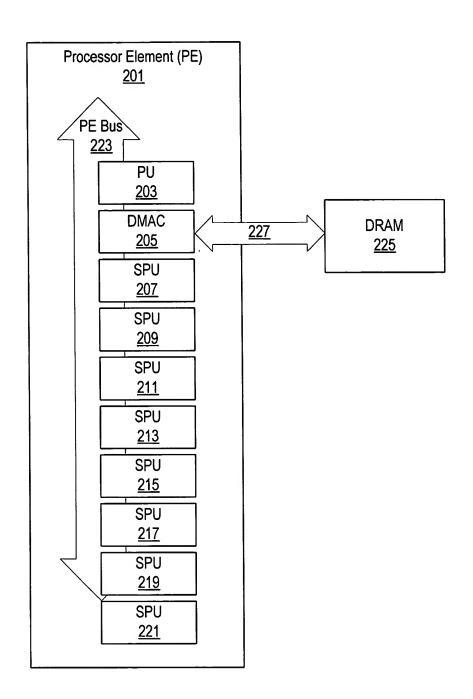


Figure 2

D cket No. AUS920030707US1 Aguilar, et al.

Syst m and Method for Managing a Plurality f Pr cessors as Devices

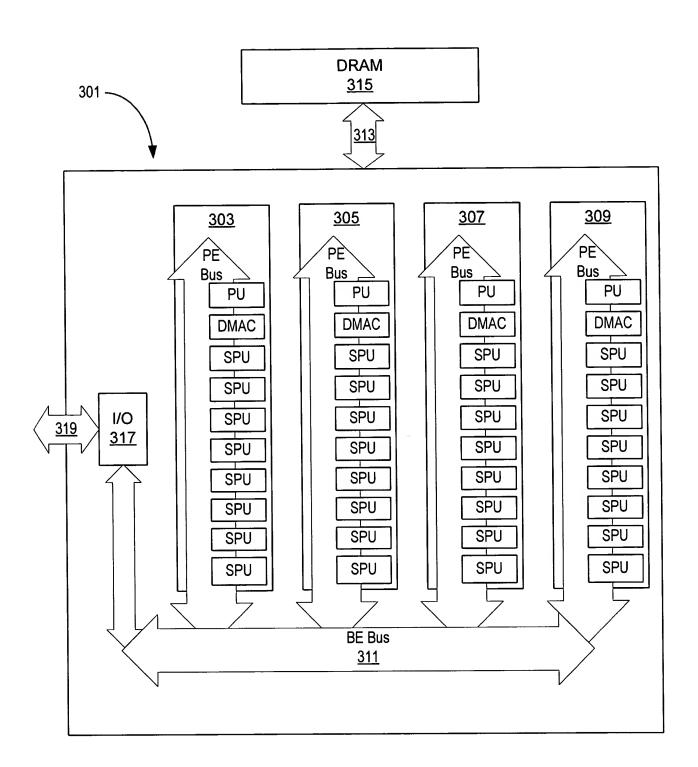


Figure 3

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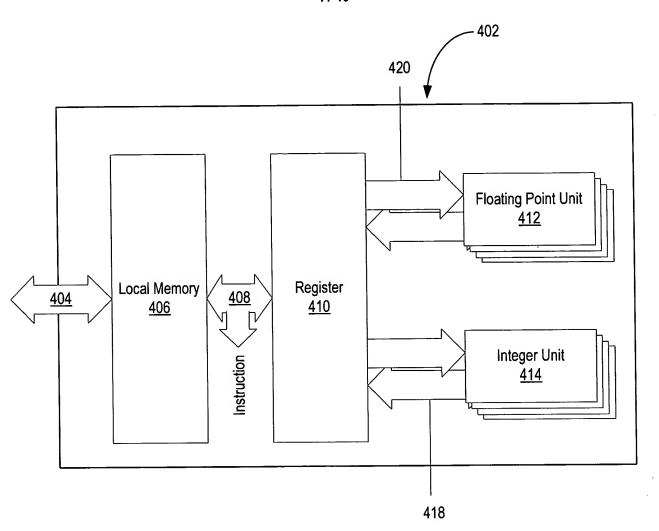


Figure 4

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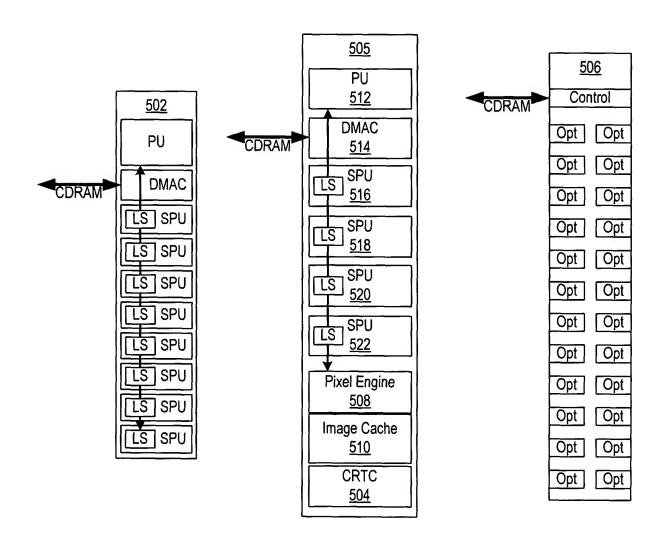
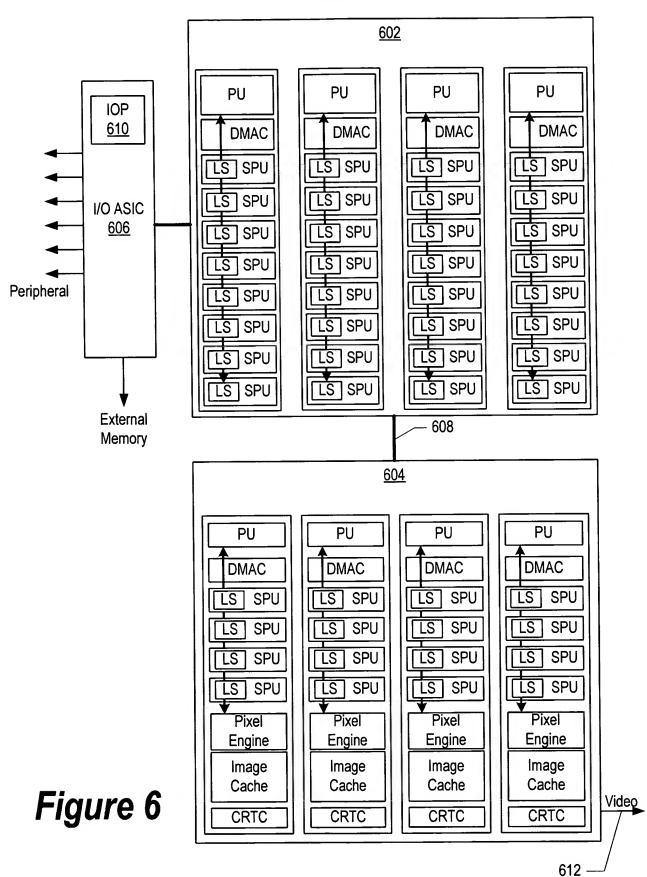


Figure 5

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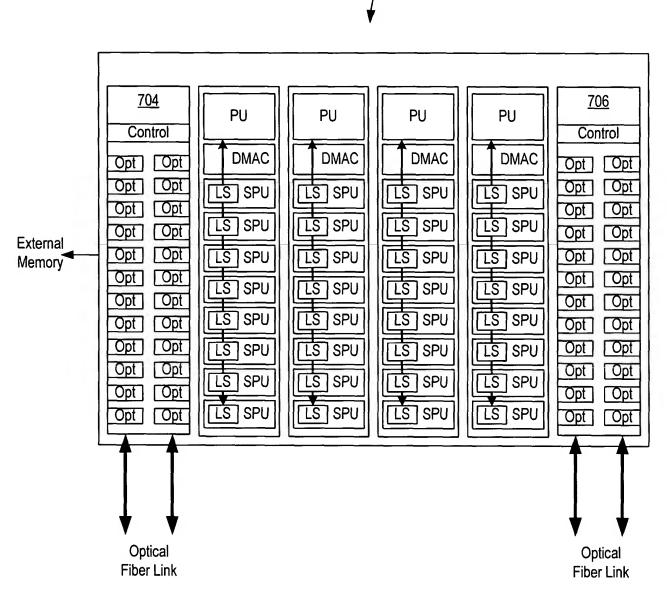


Figure 7

Docket No. AUS920030707US1

Aguilar, et al.

Syst m and Meth d f r Managing a Plurality f Proc ss rs as Devices

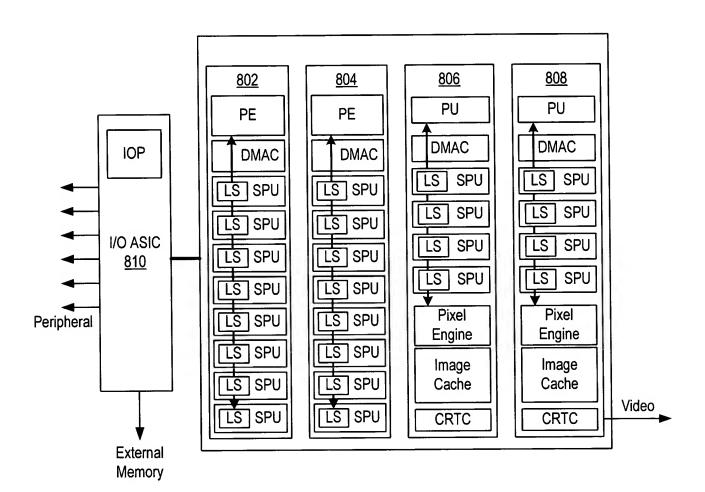


Figure 8

Docket No. AUS920030707US1
Aguilar, t al.
System and Method for Managing a Plurality of Pr c ssors as Devices

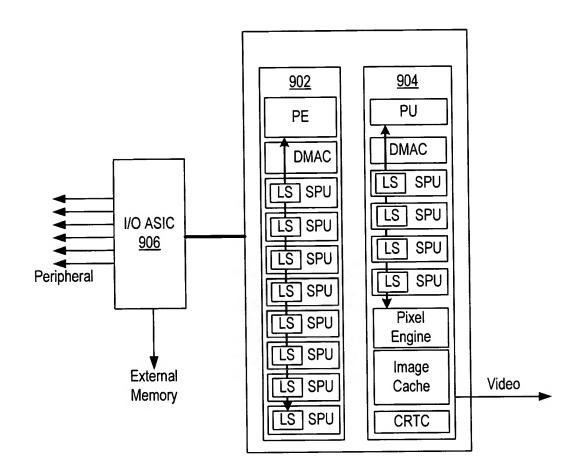


Figure 9

Docket No. AUS920030707US1

Aguilar, tal.

System and Meth d for Managing a Plurality f Pr cessors as D vic s

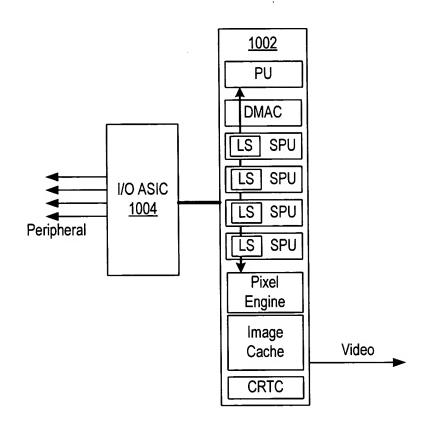


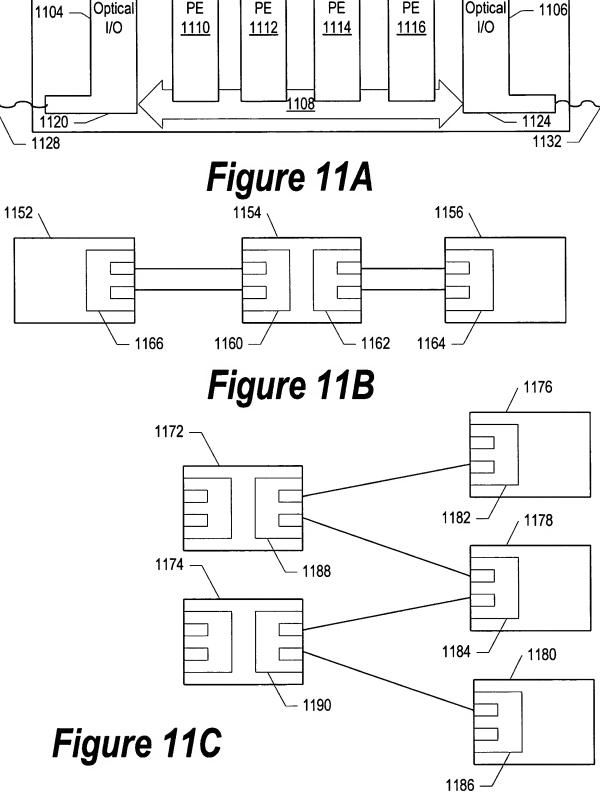
Figure 10

D cket No. AUS920030707US1

Aguilar, et al.

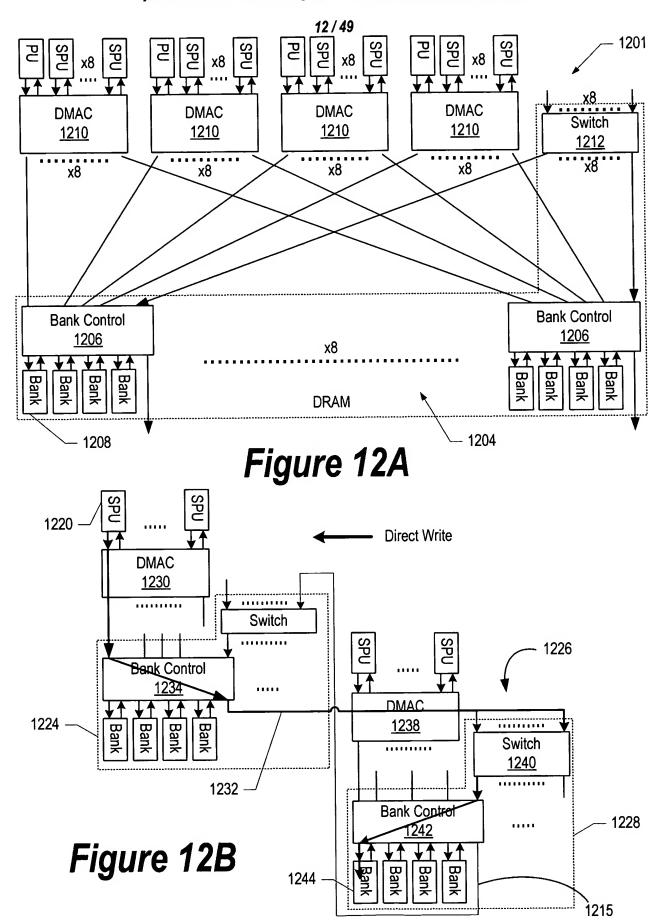
System and Meth d for Managing a Plurality of Pr c ssors as D vices

1126 1130 -11/49 1122 1118 1102 Optical PE PE PΕ PΕ Optical - 1106 1104 1/0 1110 1112 1/0 <u>1114</u> <u>1116</u> ነ108



Dock t No. AUS920030707US1 Aguilar, et al.

System and Meth d for Managing a Plurality of Proc ss rs as Devic s



Docket No. AUS920030707US1 Aguilar, et al. System and Method f r Managing a Plurality of Pr cessors as D vices

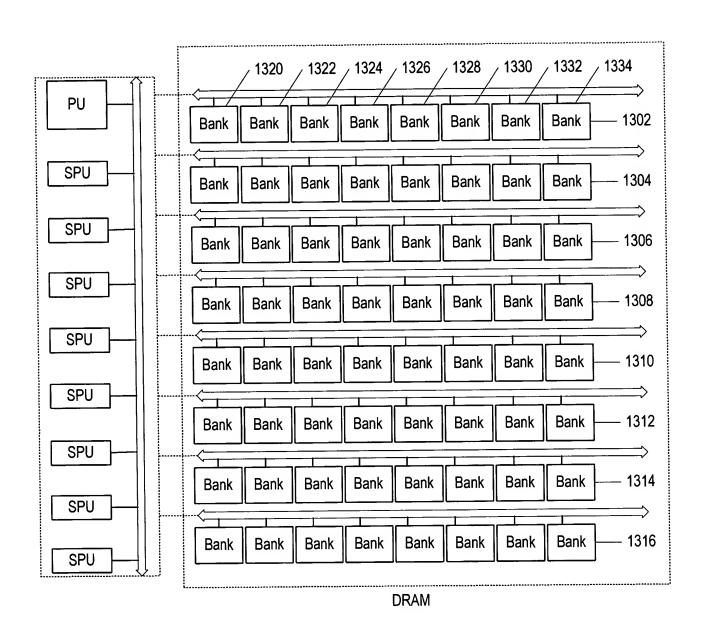


Figure 13

File: 3051.VSD Docket No. AUS920030707US1
Aguilar, et al.

System and M th d for Managing a Plurality f Pr cess rs as Devices

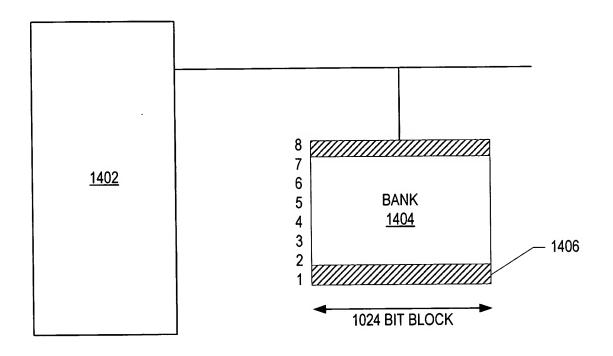


Figure 14A

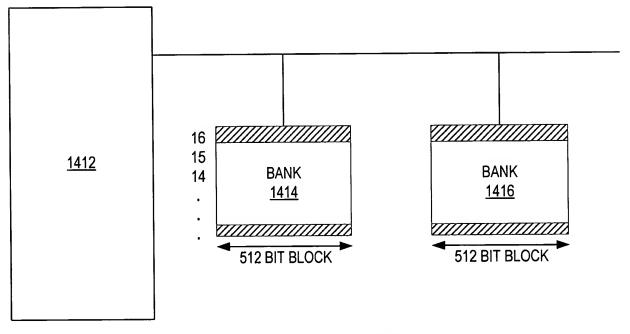


Figure 14B

Docket No. AUS920030707US1

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System and Meth d f r Managing a Plurality of Pr cessors as Devic s

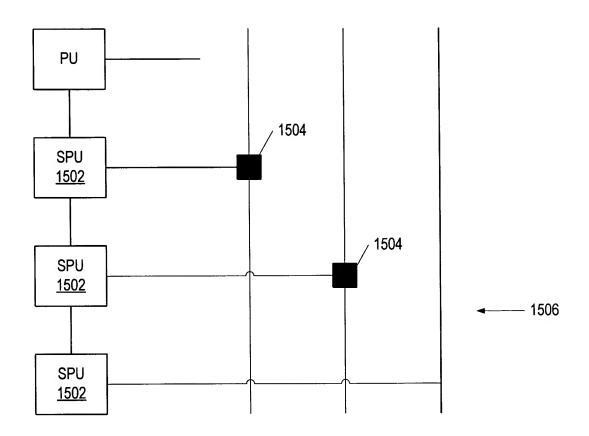


Figure 15

Docket No. AUS920030707US1

Aguilar, et al.

System and Meth d for Managing a Plurality f Pr cess rs as Devices

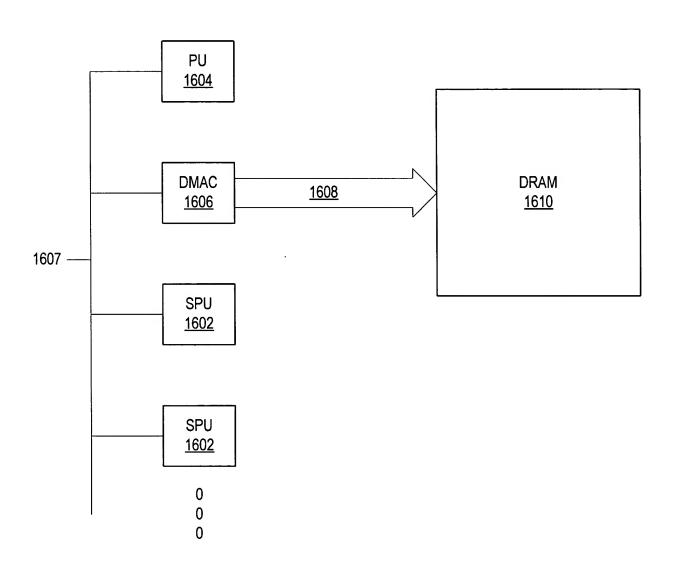
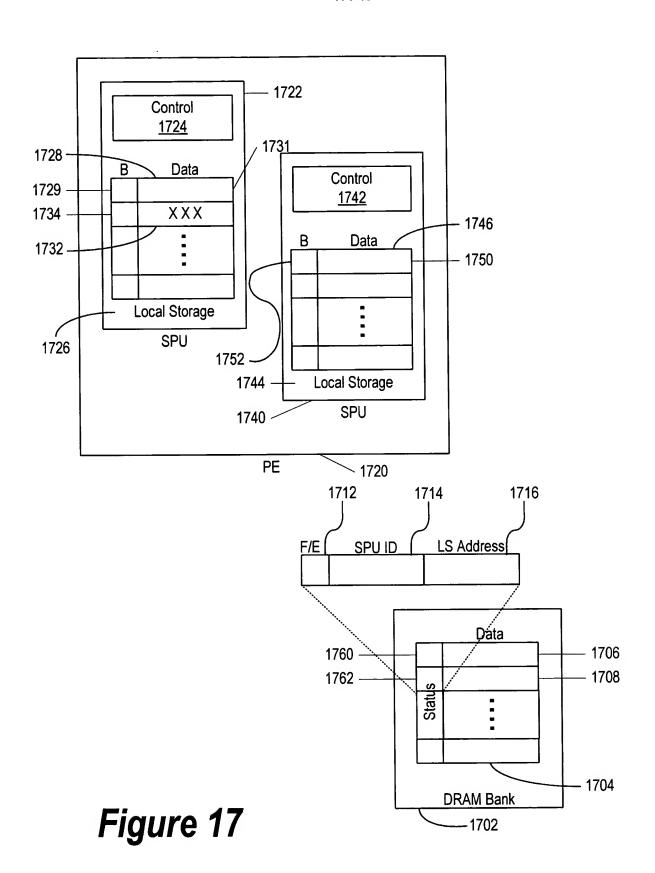


Figure 16

D cket No. AUS920030707US1 Aguilar, et al.

System and Meth d for Managing a Plurality of Pr cessors as Devic s



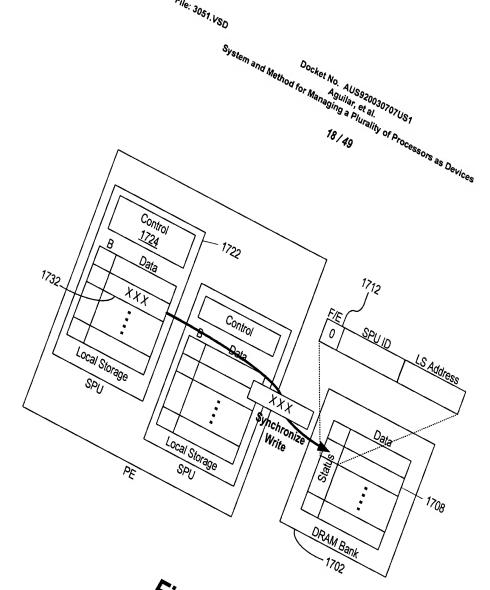


Figure 18

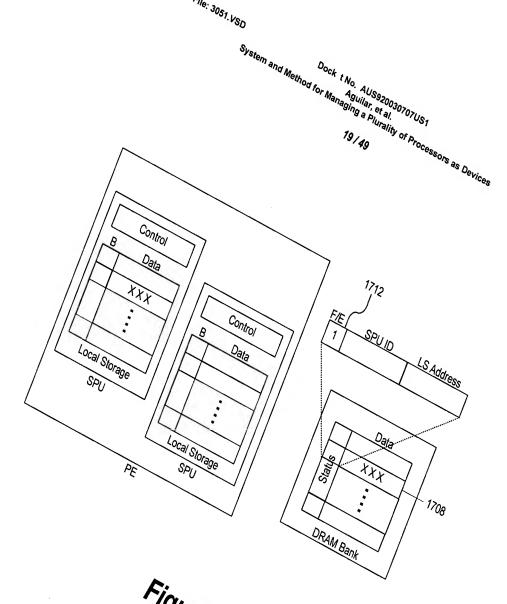
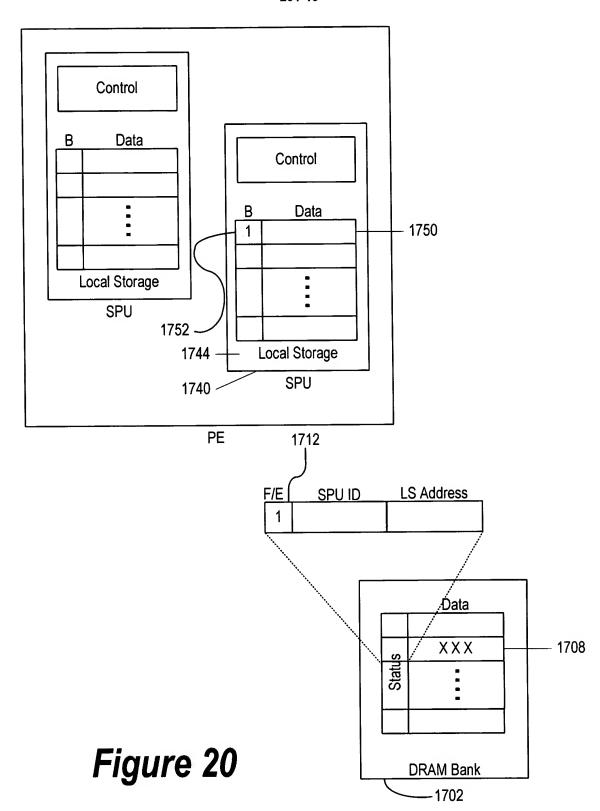


Figure 19

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System and M thod f r Managing a Plurality of Proc ss rs as Devic s

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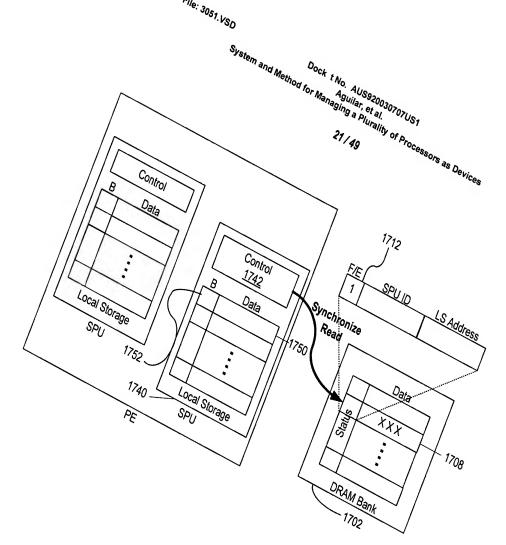
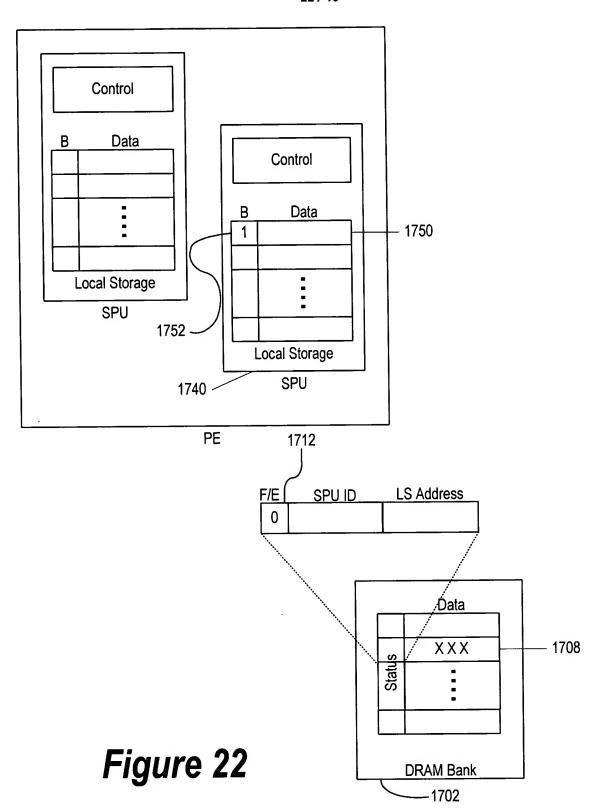
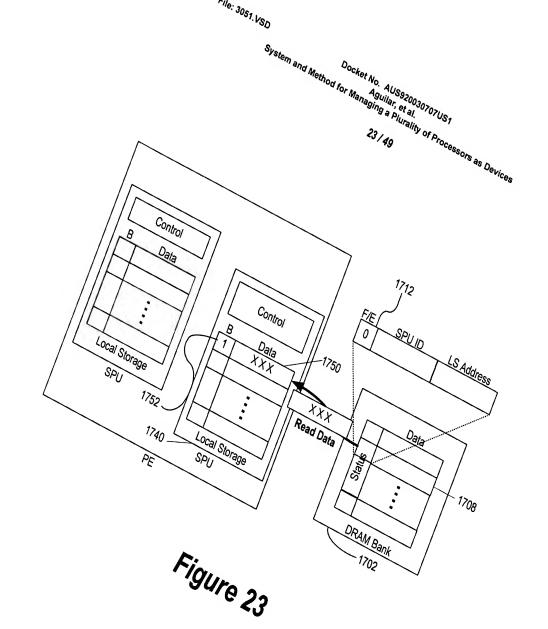


Figure 21

Docket No. AUS920030707US1 Aguilar, et al. System and Method f r Managing a Plurality of Pr cessors as Devic s

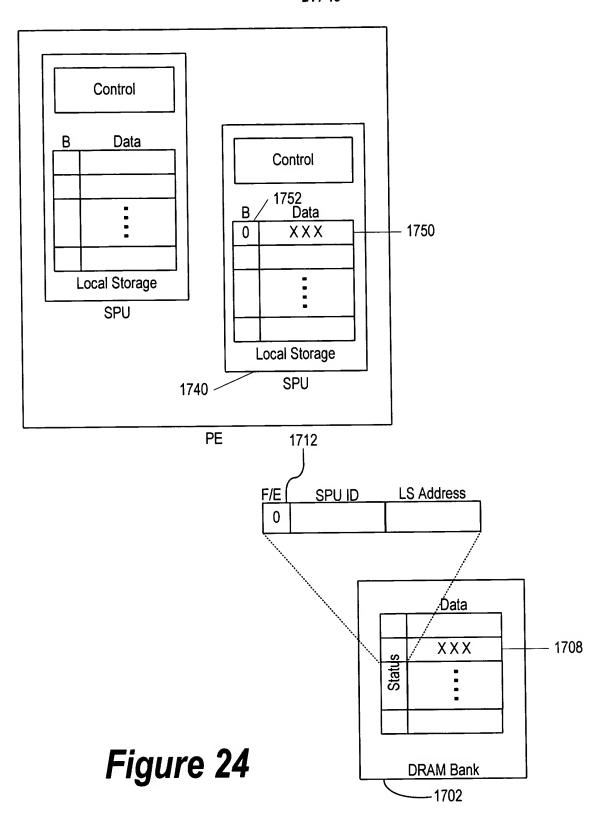




D cket No. AUS920030707US1 Aguilar, et al.

System and M thod f r Managing a Plurality of Pr cess rs as Devic s

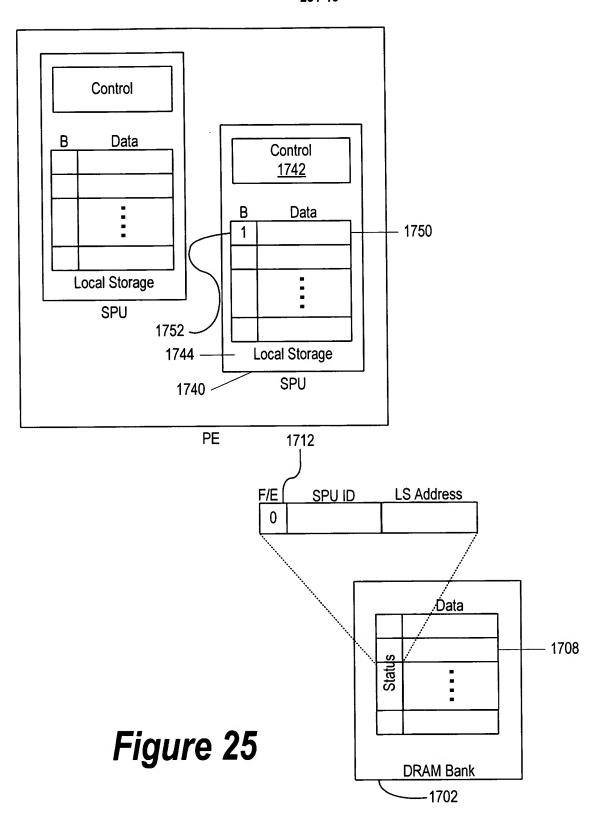
24/49



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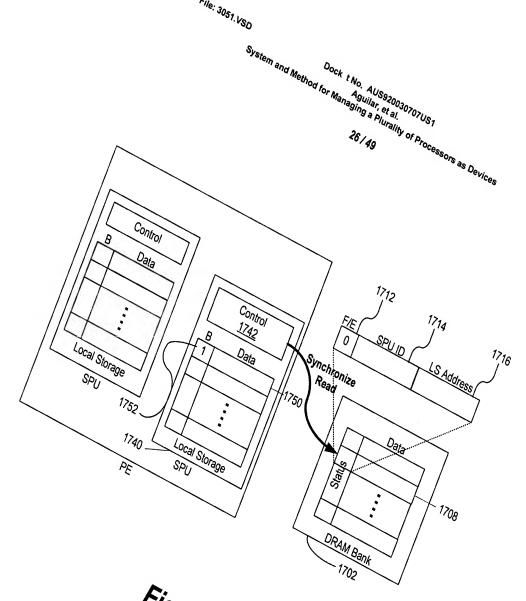


Figure 26

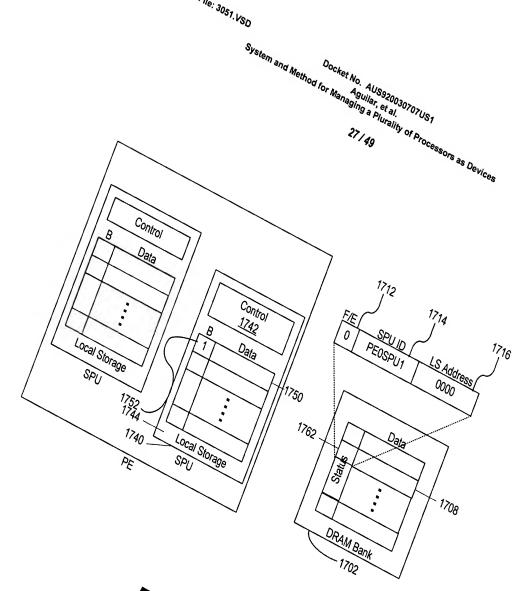


Figure 27

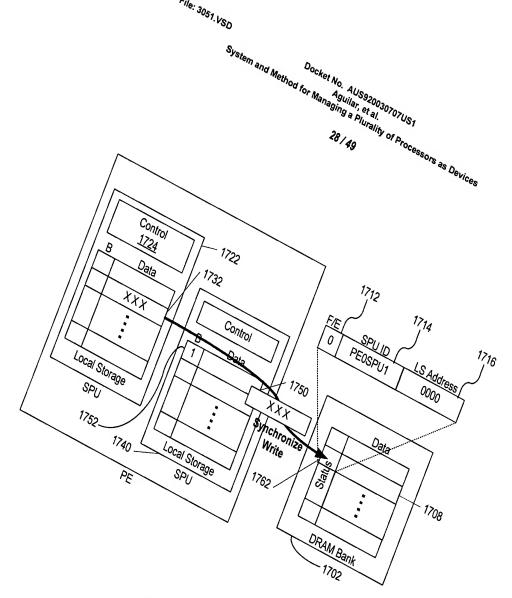


Figure 28

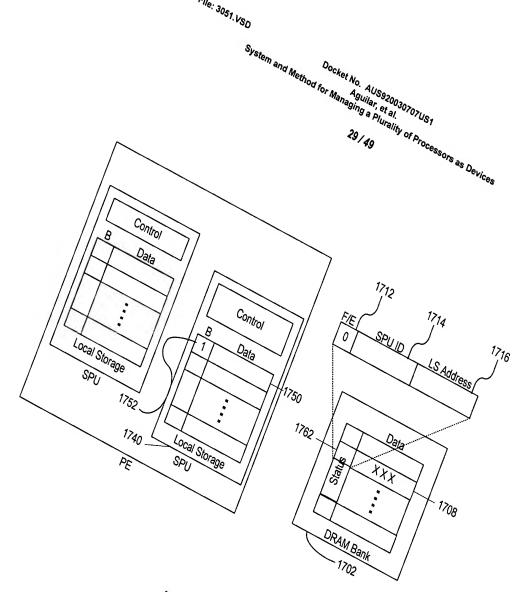


Figure 29

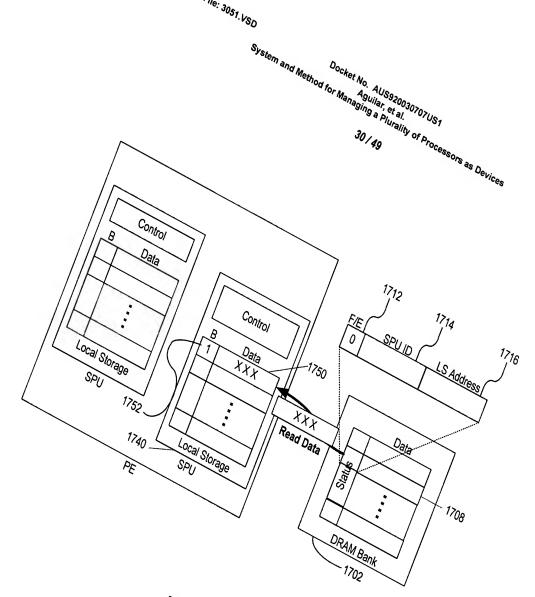


Figure 30

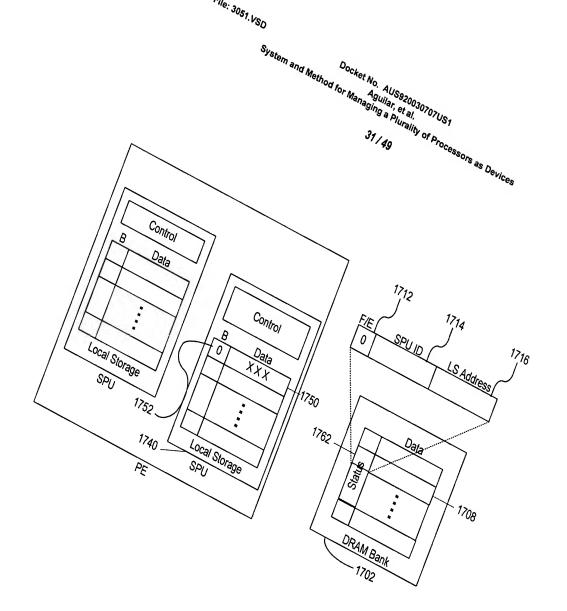


Figure 31

Docket No. AUS920030707US1

Aguilar, et al.

System and Meth d for Managing a Plurality f Process rs as Devic s

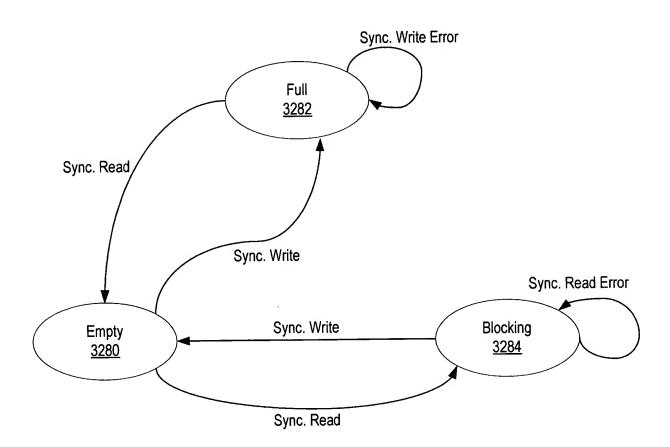


Figure 32

Docket No. AUS920030707US1

Aguilar, et al.

Syst m and M th d f r Managing a Plurality of Process rs as Devic s

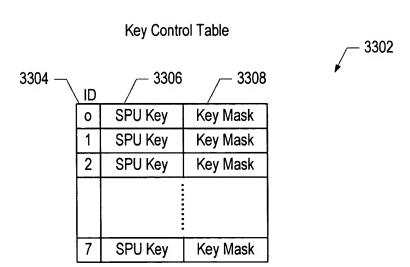


Figure 33

D ck t No. AUS920030707US1 Aguilar, et al.

System and M th df r Managing a Plurality of Pr c ss rs as Devic s

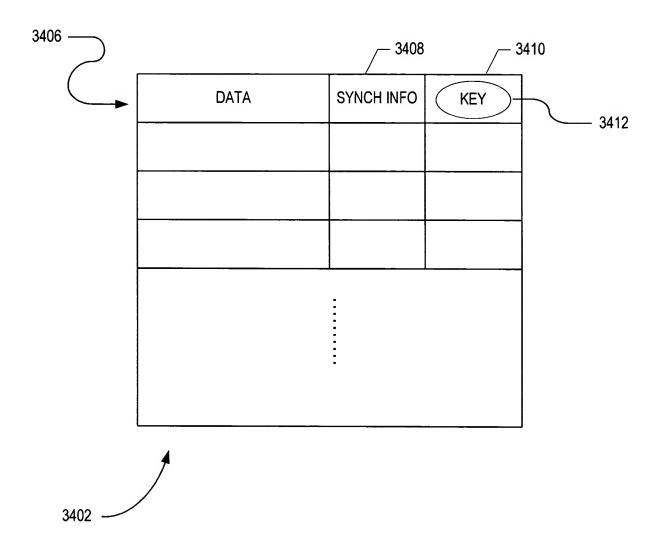


Figure 34

D cket No. AUS920030707US1

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System and Meth d f r Managing a Plurality f Pr cess rs as Devices

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Memory Access Control Table

		3506	_	3508 _ 3510	_ 3512
3504 —	\ ID				
	0	Base	Size	Access Key	Access Key Mask
	1	Base	Size	Access Key	Access Key Mask
	2	Base	Size	Access Key	Access Key Mask
	63	Base	Size	Access Key	Access Key Mask



Figure 35

File: 3051.VSD Docket No. AUS920030707US1
Aguilar, et al.

System and Meth d for Managing a Plurality f Proc ss rs as D vic s

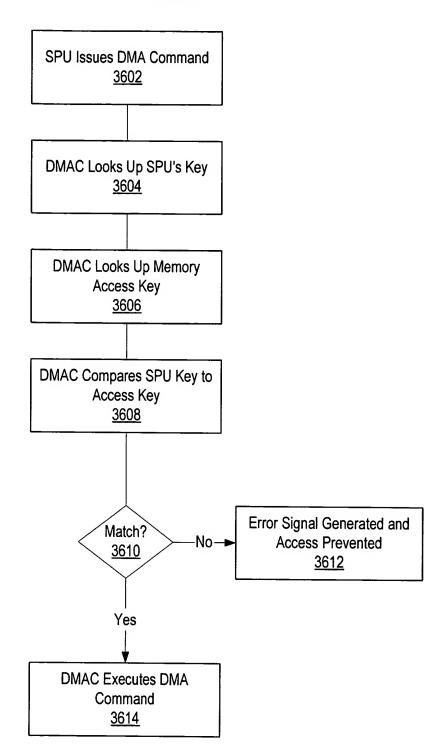


Figure 36

D cket No. AUS920030707US1 Aguilar, et al. System and M thod f r Managing a Plurality f Pr cessors as Devices

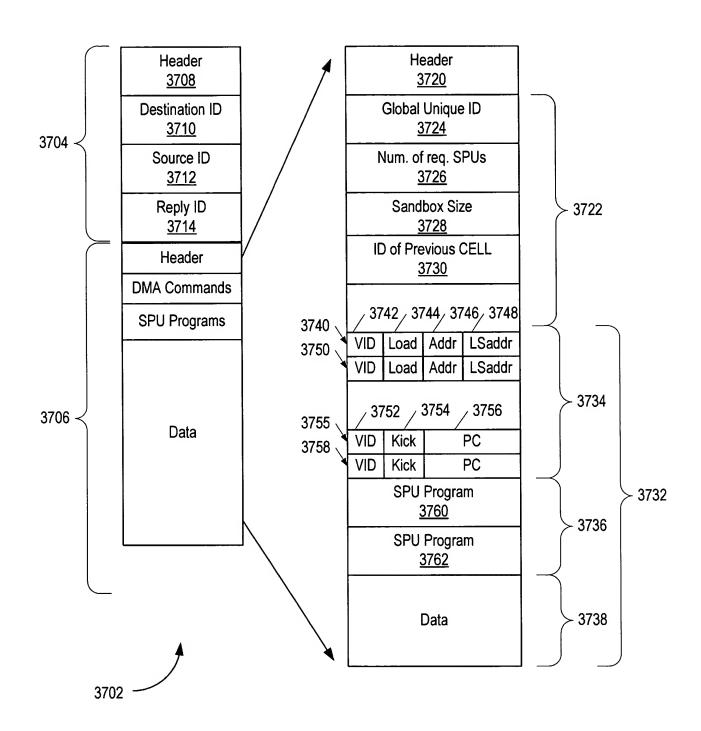


Figure 37

D cket No. AUS920030707US1
Aguilar, et al.
System and M th d f r Managing a Plurality f Processors as D vices

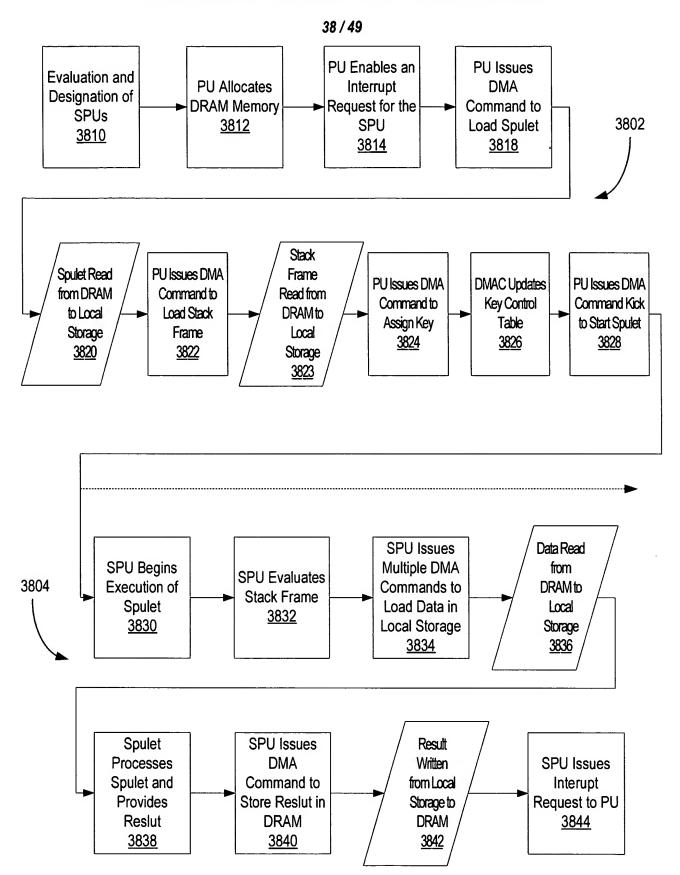
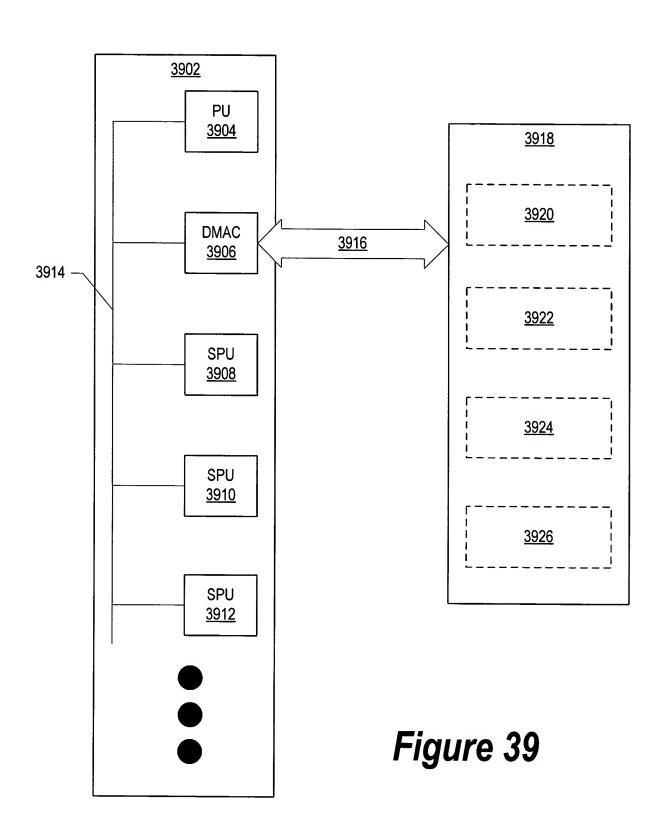


Figure 38

Docket No. AUS920030707US1 Aguilar, et al. Syst $\,$ m and $\,$ M thod $\,$ f $\,$ managing a Plurality of $\,$ Pr $\,$ c $\,$ ss $\,$ rs as Devices



D cket No. AUS920030707US1 Aguilar, et al.

System and Method f r Managing a Plurality of Processors as Devices

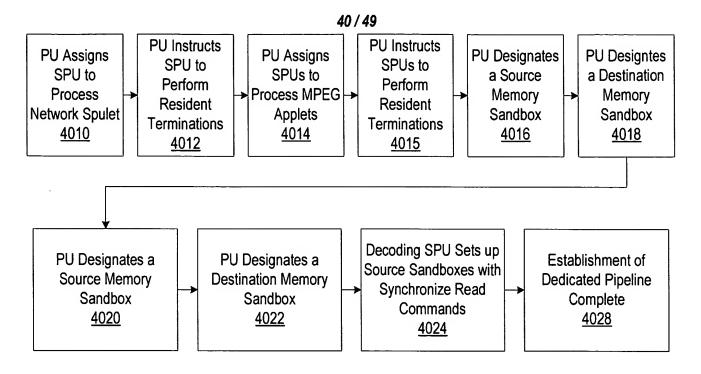


Figure 40A

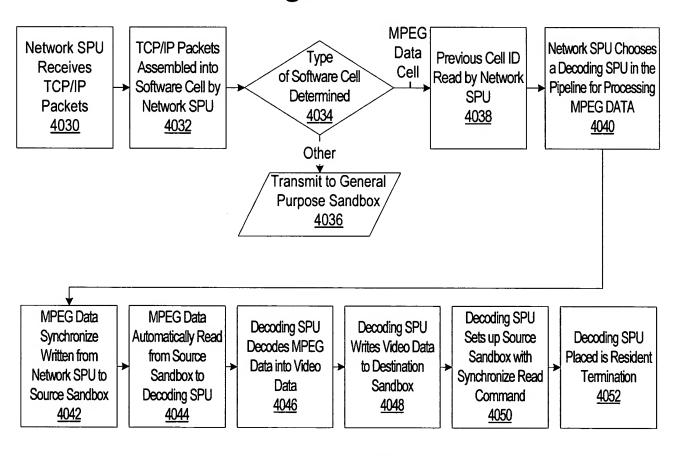


Figure 40B

Docket No. AUS920030707US1

Aguilar, et al.

System and Meth d for Managing a Plurality f Processors as D vices

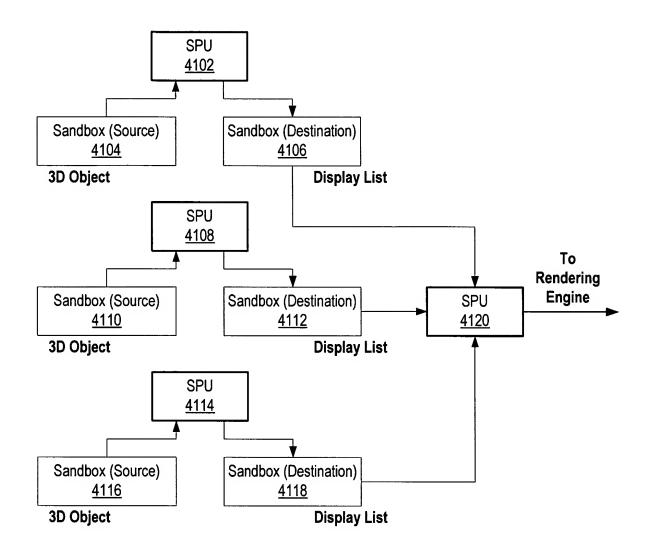


Figure 41

D cket No. AUS920030707US1

Aguilar, et al.

System and M th d f r Managing a Plurality f Pr cess rs as D vices

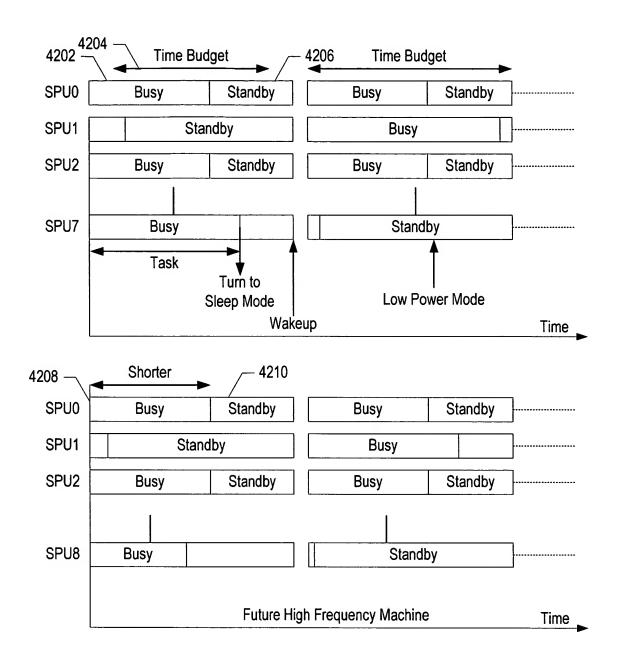


Figure 42

Docket No. AUS920030707US1 Aguilar, et al. System and Method for Managing a Plurality of Processors as Devices

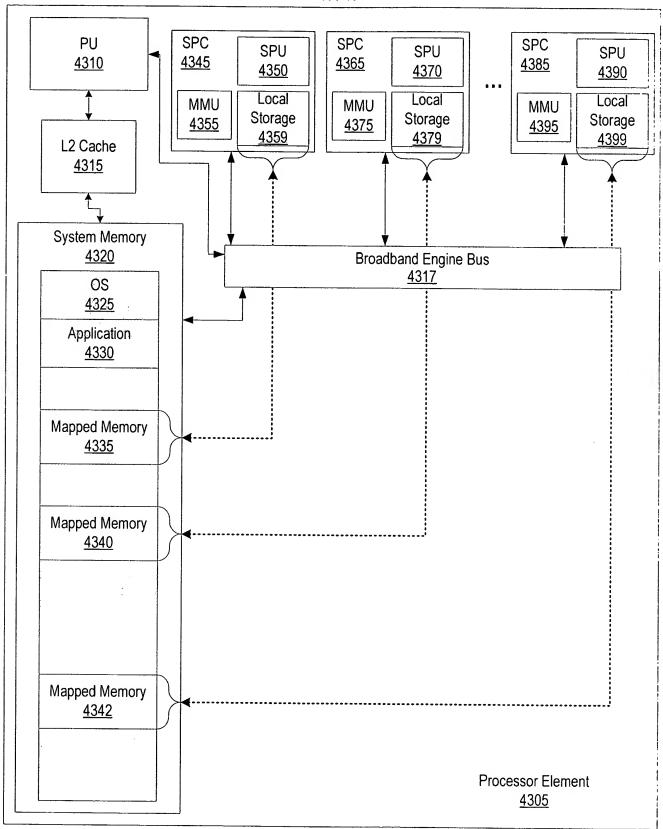


Figure 43

D cket No. AUS920030707US1 Aguilar, et al. System and M thod for Managing a Plurality f Pr c ss rs as Devices

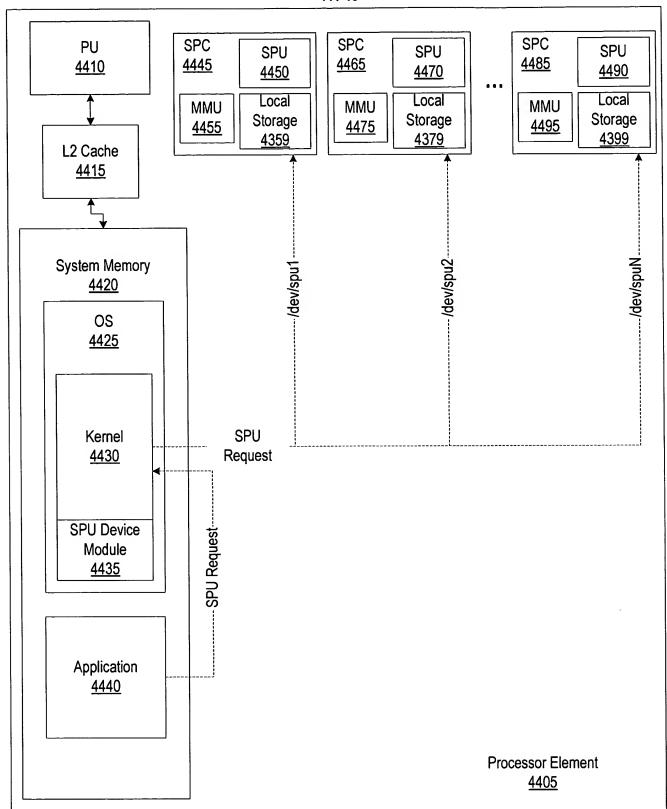


Figure 44

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Device Configuration File		4510
Device Access Path	Device] /
/dev/spu1	SPU 1]/
/dev/spu2	SPU 2	
•••	•••	
/dev/spuN	SPU N	
•••	•••	
/dev/floppy	Floppy Disk Drive	
/dev/disk	Hard Disk Drive	
•••	•••	

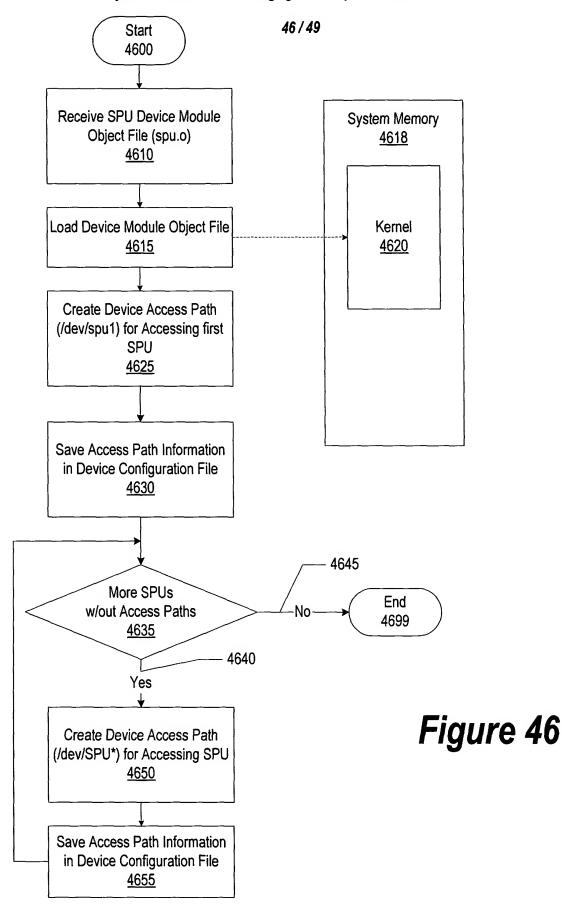
Figure 45A

Application Code	
Instructions	
write(/dev/spu2,)	4520
Instructions	_/
ioctl(/dev/spu2)	

Figure 45B

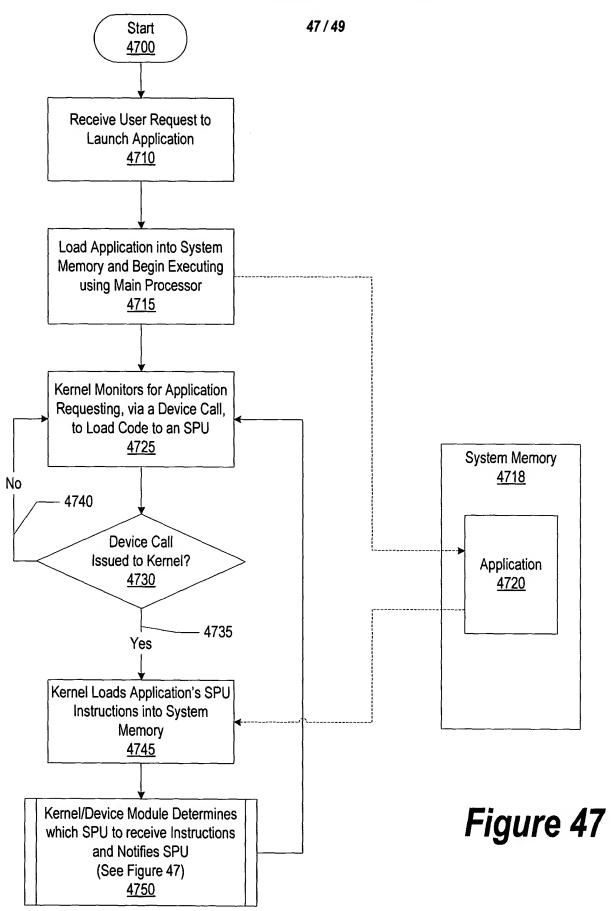
Docket N . AUS920030707US1 Aguilar, et al.

System and Meth df r Managing a Plurality f Pr cessors as Devic s



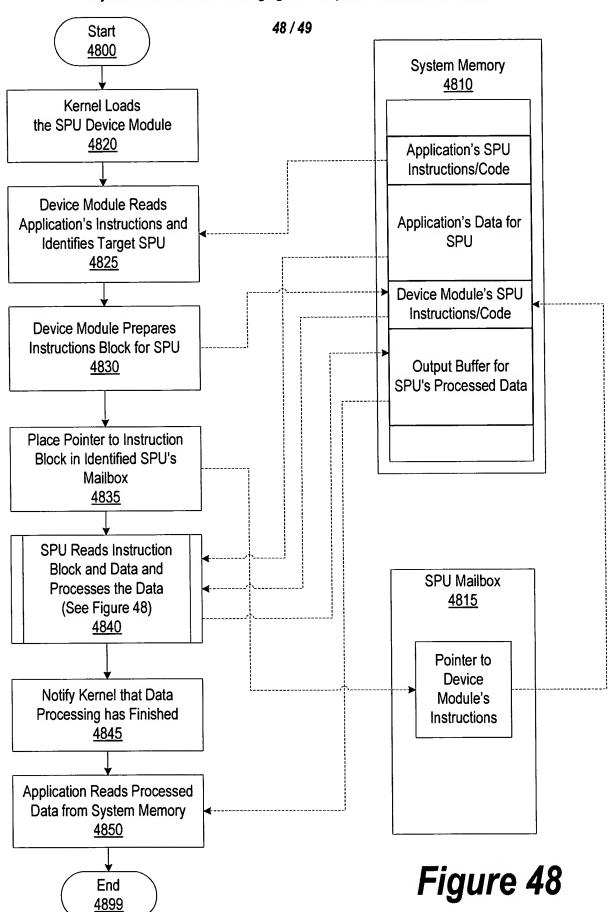
Docket No. AUS920030707US1 Aguilar, et al.

System and Meth d for Managing a Plurality of Process rs as Devices



D ck t N . AUS920030707US1 Aguilar, et al.

Syst m and Meth df r Managing a Plurality of Pr cess rs as D vic s



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System and Meth df r Managing a Plurality of Processors as D vic s

